



New Micro Architecture

77 Million Transistors



Micro-Ops Fusion

fuses operations together to enable faster execution of instructions at lower power

Advanced Branch Prediction

– fewer re-dos for increased performance

1MB Power Optimized L2 Cache

– enables higher CPU performance

Streaming SIMD Extensions II

compatible with Pentium® 4 Processor optimized software

Dedicated Stack Management

– faster instruction at lower power levels

Enhanced Intel® SpeedStep® Technology

- Multiple voltages & frequency operating points

400 MHz Power Optimized System Bus

- faster system bus to enhance performance at lower power levels

